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|-------|------|---|--------------------------------|------------------|---------|------------------|
| L1 | 105 | (multi-bit adj2 symbol\$1) | USPAT; EPO; JPO; DERWENT | OR | OFF | 2004/11/19 13:40 |
| L2 | 18 | (ECC or (error adj2 correct\$4)) same (multi-bit adj2 symbol\$1) | USPAT; EPO; JPO; DERWENT | OR | OFF | 2004/11/19 13:47 |
| L3 | 7 | (ECC or (error adj2 correct\$4) near4 encod\$4) same (multi-bit adj2 symbol\$1) | USPAT; EPO; JPO; DERWENT | OR | OFF | 2004/11/19 13:41 |
| L4 | 7 | (ECC or (error adj2 correct\$4) near3 encod\$4) same (multi-bit adj2 symbol\$1) | USPAT; EPO; JPO; DERWENT | OR | OFF | 2004/11/19 13:41 |
| L5 | 26 | (ECC or (error adj2 correct\$4) near3 encod\$4) and (multi-bit adj2 symbol\$1) | USPAT; EPO; JPO; DERWENT | OR | OFF | 2004/11/19 13:42 |
| L6 | 50 | (ECC or (error adj2 correct\$4) near3 encod\$4) and ((multi-bit adj2 symbol\$1) or (n-bit adj2 symbol\$1)) | USPAT; EPO; JPO; DERWENT | OR | OFF | 2004/11/19 13:43 |
| L7 | 11 | (ECC or (error adj2 correct\$4) near3 encod\$4) same ((multi-bit adj2 symbol\$1) or (n-bit adj2 symbol\$1)) | USPAT; EPO; JPO; DERWENT | OR | OFF | 2004/11/19 13:43 |
| L8 | 8 | (ECC or (error adj2 correct\$4) near3 encod\$4) near15 ((multi-bit adj2 symbol\$1) or (n-bit adj2 symbol\$1)) | USPAT; EPO; JPO; DERWENT | OR | OFF | 2004/11/19 13:43 |
| L9 | 0 | (ECC or (error adj2 correct\$4) near3 encod\$4) same ((multi-bit adj2 symbol\$1) or (n-bit adj2 symbol\$1)) same controller | USPAT; EPO; JPO; DERWENT | OR | OFF | 2004/11/19 13:43 |
| L10 | 5 | (ECC or (error adj2 correct\$4) near3 encod\$4) same ((multi-bit adj2 symbol\$1) or (n-bit adj2 symbol\$1)) same (buffer or storage\$1) | USPAT; EPO; JPO; DERWENT | OR | OFF | 2004/11/19 13:44 |
| L11 | 0 | (ECC or (error adj2 correct\$4) near3 encod\$4) same ((multi-bit adj2 symbol\$1) or (n-bit adj2 symbol\$1)) same row\$1 | USPAT; EPO; JPO; DERWENT | OR | OFF | 2004/11/19 13:44 |
| L12 | 1 | (ECC or (error adj2 correct\$4) near3 encod\$4) same ((multi-bit adj2 symbol\$1) or (n-bit adj2 symbol\$1)) same ((memory adj2 cell\$1) or row\$1 or (memory adj2 array)) | USPAT; EPO; JPO; DERWENT | OR | OFF | 2004/11/19 13:45 |

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|-----|-----|--|--------------------------------|----|-----|------------------|
| L13 | 4 | ((ECC or (error adj2 correct\$4) near3 encod\$4) same ((multi-bit adj2 symbol\$1) or (n-bit adj2 symbol\$1))) and ((memory adj2 cell\$1) or row\$1 or (memory adj2 array)) | USPAT; EPO; JPO; DERWENT | OR | OFF | 2004/11/19 13:46 |
| L14 | 95 | (ECC or (error adj2 correct\$4) adj2 encod\$4) same ((multi-bit adj2 symbol\$1) or n-bit\$1) | USPAT; EPO; JPO; DERWENT | OR | OFF | 2004/11/19 13:48 |
| L16 | 10 | (ECC or (error adj2 correct\$4) adj2 encod\$4) same ((multi-bit adj2 symbol\$1) or (n-bit\$1 adj2 symbol\$1)) | USPAT; EPO; JPO; DERWENT | OR | OFF | 2004/11/19 13:49 |
| L17 | 41 | (ECC or (error adj2 correct\$4) adj2 encod\$4) and ((multi-bit adj2 symbol\$1) or (n-bit\$1 adj2 symbol\$1)) | USPAT; EPO; JPO; DERWENT | OR | OFF | 2004/11/19 13:50 |
| L18 | 15 | (ECC or (error adj2 correct\$4) adj2 encod\$4) and ((multi-bit adj2 symbol\$1) or (n-bit\$1 adj2 symbol\$1)) and ((memory adj2 cells) or (memory adj2 array) or row\$1) | USPAT; EPO; JPO; DERWENT | OR | OFF | 2004/11/19 14:01 |
| L19 | 0 | (ECC or (error adj2 correct\$4) adj2 encod\$4) and (((multi-bit adj2 symbol\$1) or (n-bit\$1 adj2 symbol\$1)) near10 row\$1) | USPAT; EPO; JPO; DERWENT | OR | OFF | 2004/11/19 14:01 |
| L20 | 0 | (ECC or (error adj2 correct\$4) adj2 encod\$4) and (((multi-bit adj2 symbol\$1) or (n-bit\$1 adj2 symbol\$1)) same row\$1) | USPAT; EPO; JPO; DERWENT | OR | OFF | 2004/11/19 14:02 |
| L21 | 2 | (((multi-bit adj2 symbol\$1) or (n-bit\$1 adj2 symbol\$1)) same row\$1) | USPAT; EPO; JPO; DERWENT | OR | OFF | 2004/11/19 14:02 |
| L22 | 681 | (multi-bit same row\$1) | USPAT; EPO; JPO; DERWENT | OR | OFF | 2004/11/19 14:03 |
| L23 | 284 | (multi-bit near10 row\$1) | USPAT; EPO; JPO; DERWENT | OR | OFF | 2004/11/19 14:03 |
| L24 | 162 | (multi-bit near4 row\$1) | USPAT; EPO; JPO; DERWENT | OR | OFF | 2004/11/19 14:03 |
| L25 | 15 | (multi-bit near4 row\$1 near4 stor\$3) | USPAT; EPO; JPO; DERWENT | OR | OFF | 2004/11/19 14:05 |
| L26 | 2 | (stor\$3 adj3 multi-bit\$1 adj3 row\$1) | USPAT; EPO; JPO; DERWENT | OR | OFF | 2004/11/19 14:06 |

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|-----|---|--|--------------------------------|----|-----|------------------|
| L27 | 8 | ((stor\$3 adj3 multi-bit\$1) adj10 row\$1) | USPAT; EPO; JPO; DERWENT | OR | OFF | 2004/11/19 14:07 |
| L28 | 0 | ((stor\$3 adj3 multi-bit\$1) adj10 row\$1) and ECC | USPAT; EPO; JPO; DERWENT | OR | OFF | 2004/11/19 14:07 |

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1 A design of Reed-Solomon decoder with systolic-array structure
Iwamura, K.; Dohi, Y.; Imai, H.;

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 Pages:118 - 122

[\[Abstract\]](#) [\[PDF Full-Text \(332 KB\)\]](#) **IEEE JNL**
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Kamali, B.; Morris, P.;

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 Pages:983 - 986 vol.2

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4 Reed-Solomon codecs for optical communications
Popovici, E.M.; Fitzpatrick, P.;

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5 A Reed-Solomon decoder with the efficient recursive cell architecture for DVD application

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Pages:677 - 682

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